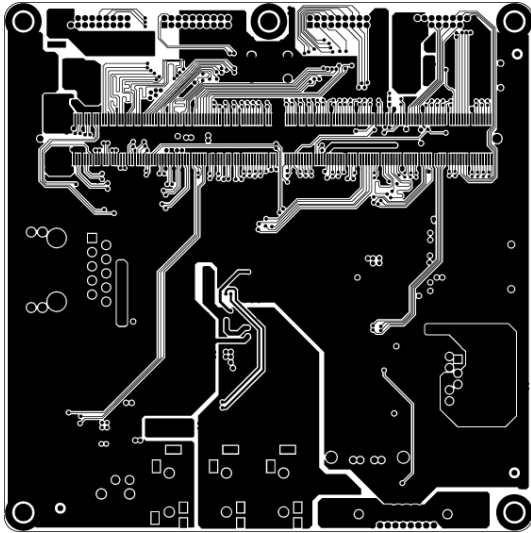
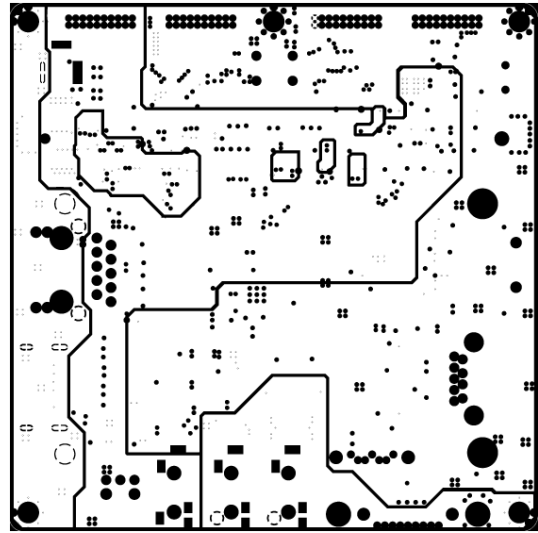




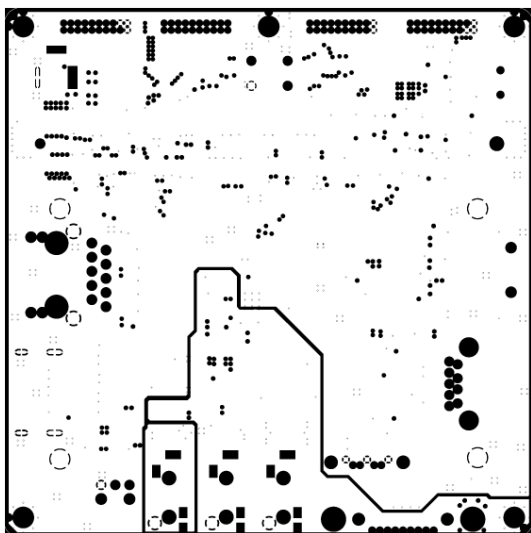
I/O Baseboard



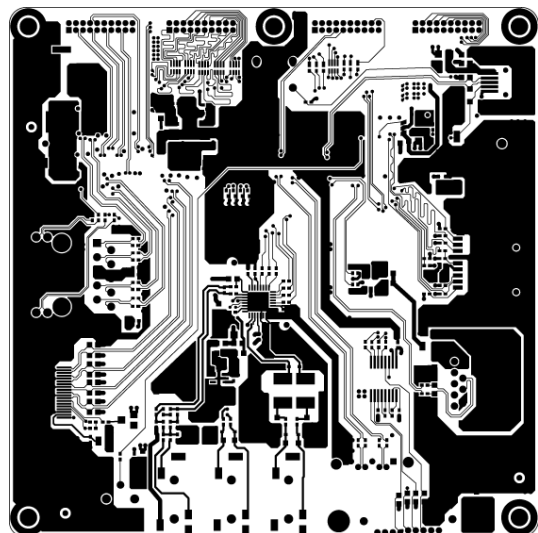
Layer 1: Top side



Layer 2: GND



Layer 3: VCC



Layer 4: Bottom side